

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : Unknown
Serial No.: Unknown Examiner : Unknown
Filed : June 25, 2001
Title : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREFOR

JC903 U.S. PTO
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06/25/01

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This filing is being made with the filing of the above-referenced application. No fee is required.

Each of the documents is either in the English language, or contains an English abstract, hence no concise explanation is necessary per rule 97(c).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

CERTIFICATE OF MAILING BY EXPRESS MAIL

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I hereby certify under 37 CFR §1.10 that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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No. 06-1050.

Respectfully submitted,

Date: 6/25/01



Scott C. Harris
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SCH/smr

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